

Name : Mr.K.Srinivasan				
Designation:	Associate Professor			
Qualification :	BE, M.Tech, (Ph.D.)			
Area of Specialization:	VLSI DESIGN			
Experience : (As on July - 2025)	Industrial Experience		Teaching Experience	
	-		17.6	
No. of workshop/FDP attended :	No. of workshops attended		No. of FDP attended	
	13		26	
Publication :	Journal		Conference	
	International	National	International	National
	8	2	4	2
Professional Membership:	1. ISTE – Life Member- LM 131464 2. IEEE-Senior Member- 98102348			
Patents:	<ol style="list-style-type: none"> 1. Published patent titled "LI-FI Based Stereo Audio Streaming Technology" – Application No. 202041050668, published in The Patent Office Journal on 04 Dec 2020. 2. Published patent titled "AI and IoT Based Wireless Sensor Network for Crop Health Monitoring" – Application No. 202341057375, published in The Patent Office Journal (2023). 3. Published patent titled "Border Aware Autonomous Speed Control and Safety Communication system for Fishermen", published in the patent office journal on 31/10/2025. 4. Published patent titled "Intelligent Security Camera", published in the patent office journal on 7/11/2025. 			

Staff Achievements:

- Successfully completed “10 Day Boot Camp on Getting Started with Data Sciences”, April 2020.
- Jury Member for *International Level Application Development Event – APP FESTO 2020*, Aug–Sep 2020.
- Faculty Advisor mentoring team *INFINIX 2.0 CODERS* in *APP FESTO 2020*, Aug–Sep 2020.
- Completed 84-hour online course on “*Basics of Remote Sensing, GIS, and GNSS*” by IIRS-ISRO, Aug–Nov 2020.
- Recognized as one of the *225 Most Consistent and Active Mentors* in *ATL Tinkerpreneur 2021 Summer Bootcamp* by NITI Aayog.
- Delivered Guest Lecture on “*MOSFET Evolution*” at Rajalakshmi Engineering College, Chennai, Jan 2023.
- Recognized as *Top Performing Mentor* for NPTEL Online Certification Course *Deep Learning*, July–Dec 2023.
- Recognized as *Top Performing Mentor* for NPTEL Online Certification Course *Deep Learning*, July–Dec 2024.
- Mentored and contributed as *Mentor* in *Smart India Hackathon 2024 Grand Finale*, Dec 2024.
- Provided mentorship to participants of *ATL Tinkerpreneur 2024*, recognized by NITI Aayog.
- Reviewer for the *10th International Conference on Communication and Signal Processing (ICCSP 2024)*, Apr 2024.
- Proctor for the *IEEEExtreme 18.0 Programming Competition*, Oct 2024.
- Recognized as *NPTEL Motivated Learner* for consistent performance and commitment to lifelong learning, Jan–Apr 2025.
- Reviewer for the *11th International Conference on Communication and Signal Processing (ICCSP 2025)*, Jun 2025.
- Supervisor and motivator for the student project “*Fisherman Tracking and Communication System*” under SDG Goal 16 in *Sairam SDG Solveathon 4.0*, Apr 2025.
- Co-ordinator for the 28th National Level Technical Symposium *Zenista’25* in SEC ECE Dept, 8th Aug 2025.
- Recognized as *Mentor* for NPTEL Online Certification Course *Machine Learning & Deep Learning – Fundamentals and Applications*, July–Dec 2025.
- Successfully Completed SWAYAM-NPTEL **Domain** Certification in **VLSI Design**, The Course were Completed in the period October 2025.
- Recognized as a NPTEL Discipline Star, July-Dec 2025.
- Successfully Completed AICTE – NITTT Certification (20212115361) - 11/2023/1/M1-M8/24401.
- Participated TIEMA & SAIRAM Associated Mass Cleaning campaign on Sep 8 to 12th, 2025.
- Completed Course on Panchayat Administration and Rural Development Schemes, Conducted by SIRD and Panchayat Raj on Sep 2025.
- Acted as a co-ordinator for the Department Association Inauguration Program on 24th July 2025.
- Acted as an Event co-ordinator for the IEEE CAS Society DLP on Chiplet Design Overview held on 12.02.2025

	<ul style="list-style-type: none"> • Acted as an Event co-ordinator for the IEEE CAS Society DLP on Analog and Mixed Signal Circuits and Systems for Emerging Applications held on 16.12.2025. • Successfully completed the UNESCO MGIEP course on SEEK, Oct – 28th – 2025. • Participated in the online quiz on VBYLD (2026) conducted on Mybharat on 14th October 2025. • Co-ordinator for the LEO CLUB – LEO LEADERSHIP Conclave 2026 Event, held on 20.2.2026.
<p>Publications: International Journals:</p>	<ul style="list-style-type: none"> ○ 2020 <ul style="list-style-type: none"> ○ Jayachandiran U, Srinivasan K — <i>Design of Distributed CMS Using Intelligent Content Management Components</i>, TEST Engineering & Management, Vol. 82, pp. 4672–4675, Jan–Feb 2020. ○ 2021 <ul style="list-style-type: none"> ○ K. Srinivasan, Sreeram N, Prashanth R, Subramonian B — <i>Smart Trolley Using IoT</i>, International Journal of Advanced Research in Science, Engineering and Technology, Vol. 3, Issue 1, pp. 580–583, 2021. ○ 2022 <ul style="list-style-type: none"> ○ Srinivasan K — <i>Systematized VLSI Architecture for AES Algorithm</i>, International Journal of Science and Innovative Engineering & Technology, Sep 2022. ○ 2023 <ul style="list-style-type: none"> ○ Srinivasan K — <i>Design and Implementation of RISC-V Processor ALU using Multiplexer and LUT</i>, International Journal of Novel Research and Development, Sep 2023, IJPublication. ○ Kumar A, Sudharsanam D, Naveen Kumar M, Srinivasan K — <i>Smart Helmet Based on IoT Technology</i>, International Educational Scientific Research Journal, Vol. 9, Issue 6, Jun 2023. ○ 2025 <ul style="list-style-type: none"> * Srinivasan K, Thrishanth S, Thameswer U, Lingesh M, ELANGO VAN K, Sept-Oct 2025. <i>ANDROID BASED PICK AND PLACE ROBOTIC ARM VEHICLE</i>. International Journal of Scientific Research & Engineering Trends, Volume 11, Issue 5. ○ Srinivasan, K., & Raja, J. (2025). <i>Optimized AES Cryptosystem on FPGA Using FQSR-KAT Network for Enhanced Security and Performance</i>. IETE Journal of Research, 1–12. https://doi.org/10.1080/03772063.2025.2546159

National Journal Papers:

- Srinivasan, K., Raja, J. (2026). *An Examination of Encryption Standards: AES and RSA—A Comprehensive Review*. In: Sabherwal, P., Vashisth, S., Agrawal, M., Kaushal, H. (eds) Proceedings of Smart and AI Enabled Technology for Sustainable Development . SAIT 2023. Lecture Notes in Electrical Engineering, vol 1452. Springer, Singapore. https://doi.org/10.1007/978-981-96-9370-2_10

- **2020**

- Sangeetha R, K. Srinivasan, Suguna K — *An Optimal Driving System Using Wireless Helmet, Studies in Indian Place Names*, Vol. 40, Issue 1, pp. 93–97, 2020.

- **2019**

- Srinivasan K, Bhavan S, Gireesh Shankar B, Roshini R — *Rescue Robot, JETIR*, Vol. 6, Issue 6, pp. 25–27, 2019.

International Conference Papers:

- **2025**

- Participated and Presented Paper Titled “Motorized Wheelchair with Detachable Stem” in the International conference on INTEND 2.0 organized by IETE Chennai Centre in Association with National Institute for Empowerment of Persons with Multiple Disabilities at Jerusalem College of Engineering , Chennai on 19.9.2025 & 20.9.2025.
- Participated and Presented a paper entitled “INBUILD FISHERMEN TRACKING AND COMMUNICATION SYSTEM WITH BORDER DETECTION AND WEATHER UPDATES” in the International Confence on Power Control & Computing Technologies 2025, Organised by Dept of EEE, SONA College of Technology, Salem held on 4 & 5th December 2025.

- **2024**

- K. Srinivasan, A. Akash, P. Jaiganesh, M. Mahizhan — *A VLSI Perspective on*

Faculty Development Program & STTP:	2019	<ul style="list-style-type: none"> ○ Participated in Workshop on Sensor Network Platforms & Tools, hosted by Sri Sairam Engineering College (24 Oct 2020).
	2015	<ul style="list-style-type: none"> ○ Participated in One Day Workshop on eSim by IIT Bombay at Dr. MGR Educational and Research Institute University (21 Sep 2019).
	2015	<ul style="list-style-type: none"> ○ Participated in One Day Workshop on LabVIEW Tool and Its Applications at Sri Venkateswara College of Technology (14 Feb 2015). ○ Participated in One Day Workshop on Science of EMI at Sri Sairam Engineering College (24 Sep 2015).
	2012	<ul style="list-style-type: none"> ○ Participated in DRDO-Sponsored Workshop on Recent Trends in Antennas for Multi-field Applications at Sri Sairam Engineering College (27–28 Dec 2012). ○ Participated in One Day Workshop on Recent Trends in Wireless Communication at Jerusalem College of Engineering (21 Mar 2012).
	✓ 2014	<ul style="list-style-type: none"> ○ Attended FDP on <i>Cloud Computing</i>, conducted by TCS & Sri Sai Ram Engineering College – May 2014
	✓ 2015	<ul style="list-style-type: none"> ○ Participated in FDP on <i>Embedded Systems with ARM7 Microcontrollers</i>, Pantech ProEd & Sri Sai Ram Engineering College – Jun 2015
	✓ 2018	<ul style="list-style-type: none"> ○ Completed <i>NPTEL Certification Course</i> (Aug–Oct 2018)
	✓ 2020	<ul style="list-style-type: none"> ○ Attended FDP on <i>Get Ready for AI with MATLAB and SIMULINK</i>, Sri Sai Ram Engineering College – Jun 2020 ○ Completed 2-week FDP on <i>Recent Trends in ICT and Teaching Competency</i>, Sri Sai Ram Engineering

College – May 2020

- Completed AICTE-STTP on *Recent Advances in Computational Electromagnetics for Microwave Antennas*, SVCE – Aug 2020

✓ 2021

- Participated in *Short-Term Training on AUV and Smart Systems*, SSN College – Jan 2021
- Completed AICTE-sponsored *STTP on FPGA-based Digital System Design with HDL*, Vel Tech – Feb 2021
- Attended FDP on *Advances in Photonics and Fiber Optics Technologies*, IETE & IEEE Madras – Jun 2021
- Completed AICTE ATAL FDP on *FPGA-Based Deep Learning in Signal Processing*, KLE Tech – Jul 2021
- Completed AICTE ATAL FDP on *Machine Learning Techniques in VLSI Design*, MS Ramaiah – Jul 2021

✓ 2022

- Participated in FDP on *Embedded System Design using Intel SoC FPGAs*, SSN College & Intel – Feb 2022

✓ 2023

- Participated in FDP on *Hands-on Training on Data Science Tools*, Sri Sai Ram Engineering College – Jan 2023
- Attended FDP on *Vehicular Ad-Hoc Networks (VANETs) & IoT*, VIT Chennai – Apr 2023
- Completed AICTE FDP on *Universal Human Values (UHV-II)* – Mar 2023
- Completed FDP on *Enhancing IoT Use Cases with AR/VR for Smart Applications*, VIT – Oct 2023
- Completed AICTE ATAL FDP on *Cybersecurity in Cloud Computing*, Sri Sai Ram Engineering College – Nov 2023
- Completed AICTE ATAL FDP on *Quantum Computing and its Future Scope*, Sri Sai Ram Engineering College – Dec 2023
- Completed AICTE ATAL FDP on *Exploring Semiconductor Design Using VLSI EDA Tools*, Vel Tech – Dec 2023

✓ 2024

- Participated in 3-day FDP on *Mentoring Projects for Impact*, ICT Academy – Feb 2024

<p>Seminars/Webinar & Others:</p>	<p>✓ 2025</p> <ul style="list-style-type: none"> ○ Participated in 6-day FDP on <i>Semiconductor Technologies</i> – Jul 2024 ○ Attended 6-day FDP on <i>IoT: Emerging Trends and Challenges</i>, Sri Sai Ram Engineering College – Jan 2025. ○ Successfully Participated in the one week National Level FDP on AI Tools organized by Sri Sairam Engineering College, 17th to 21st Feb 2025, NFDP-BSA107. ○ Participated one week Online FDP on “Quantum Computing and its Applications” SIT – Sep 2025. ○ Completed NPTEL – AICTE, FDP on <i>Microelectronics: Devices to Circuits</i>, July – Oct 2025, Roll No: NPTEL25EE110S554001114. ○ Participated Online FDP on <i>AI & Machine Learning Applications in Cybersecurity</i> Organized by the Dept of CSE (Cyber Security), SEC From 15.12. 2025 to 20.12.2025. <p>2025</p> <ul style="list-style-type: none"> ○ Completed 12-week NPTEL course on <i>Digital Electronic Circuits</i> with 57% score (Jan–Apr 2025). ○ Completed 12-week NPTEL course on <i>Microprocessors and Interfacing</i> with 60% score (Jan–Apr 2025). ○ Completed 12-week NPTEL course on <i>Microprocessors and Microcontrollers</i> with 55% score (Jan–Apr 2025). ○ Served as Reviewer in the 11th <i>ICCSP 2025 International Conference</i> at Adhiparasakthi Engineering College. ○ Coordinated the Technical Talk on Global AI Surge by Ms. Gollapudi Anisha (Walmart Global Tech India), IEEE Education Week, 12 Apr 2025. ○ Co-coordinated the One Topic – One Lecture session on <i>Robotics and Automation</i>, IEEE CAS & IT Societies, 18 Mar 2025. ○ Completed 12-week NPTEL course on <i>Power Electronics with Wide Band Gap Devices</i> with 62% score (July–Oct 2025). ○ Completed 12-week NPTEL course on <i>Microelectronics: Devices to Circuit</i> with 77% score (July–Oct 2025).
---------------------------------------	---

2024

- Coordinated the **IEEE DEIS Career Guidance Event Future-ON (Dielectric Fiesta)** at Swami Vivekananda School, Kancheepuram, 23 Oct 2024.
- Coordinated the **Distinguished Lecturer Program on Boosting Energy Harvesting Efficiency**, Sri Sairam Engineering College, 03 Sep 2024.
- Served as **Reviewer** for *International Conference IC3IoT-2024* at Sri Sairam Engineering College, Apr 2024.
- Completed **NPTEL** 8-week course *System Design through Verilog* with 73% score (Jul–Sep 2024).
- Served as **Reviewer** at the 10th *ICCSP 2024 International Conference on Communication and Signal Processing*.

2023

- Completed **NPTEL** 12-week course on *Deep Learning*, Jul–Oct 2023.
- Participated in **VLSI Design Flow using Vivado** webinar by Xilinx and CoreEL Technologies, Jan 2023.
- Completed **ATAL FDP** on *Semiconductor Design Using VLSI EDA Tools* at Vel Tech Multi Tech, Dec 2023.
- Successfully completed **NAAC Awareness Programme for Faculty** organized by MMIT, Pune.

2022

- Participated in **VLSI Placement Core Training** conducted in association with Tessolve Semiconductor, Mar 2022.

2021

- Completed **AICTE STTP** on *FPGA-Based Digital System Design with HDL* at Vel Tech Multi Tech, Feb 2021.

2020

- Participated in **ICT Academy Skycampus Season 5** expert session on *Future of Skills*, May 2020.
- Participated in **ICT Academy Power Seminar** on *Employability Skills for the Future*, May 2020.
- Attended **two-day webinar** on *3D EM Simulation using CST Software* organized by SVCE, May 2020.

	<ul style="list-style-type: none"> ○ Attended 3-day Arduino Virtual Workshop conducted by PSCMRCET and IEEE Student Chapter, Jun 2020. ○ Participated in a Webinar on Literature Writing conducted by Sri Sairam Engineering College. ○ Participated in academic programs conducted by Sri Sairam Engineering College. ○ Participated in academic activities organized by SJB Institute of Technology, Bangalore. ○ Coordinated the Workshop on Sensor Network Platforms & Tools by Dr. P. Vetrivelan (VIT Chennai), hosted online, 24 Oct 2020. ○ Served as Assistant Professor at Sri Sairam Engineering College, Chennai during May 2020. <p>2016</p> <ul style="list-style-type: none"> ○ Received Appreciation Letter for coordinating an industrial visit to BSNL RTTC, Maraimalai Nagar, on 09 Feb 2016. <p>2015</p> <ul style="list-style-type: none"> ○ Attended a one-day seminar on <i>Embedded Linux on ZYNQ Board</i> at VIT Chennai, in collaboration with CoreEL and Digilent Inc., 18 Sep 2015. <p>2013</p> <ul style="list-style-type: none"> ○ Successfully completed an Industrial Visit at BSNL RTTC, Maraimalai Nagar, on 16 Aug 2013. ○ Participated in <i>ISO 9001:2008 Quality Management System & Audit Training</i> as Internal Auditor, Feb 2013. <p>2009</p> <ul style="list-style-type: none"> ○ Completed a 120-hour course on <i>Windows Application Development using VC#.Net</i> from NIIT, Jul 2009.
--	--

--	--

