

Name: S.Saranya				
Designation:	Assistant Professor			
Qualification:	M.E.Ph.D. (Pursuing)			
Area of specialization:	VLSI DESIGN			
Experience : (As On September 2020)	Industrial Experience		Teaching Experience	
	NIL		8.2 YEARS	
Number of workshop / FDP attended:	Number of Workshops		Number of FDPs	
	3		10	
Publications:	Conference		Journal	
	National	International	National	International
	3	6	NIL	4
Patents:	National		International	
	NIL		NIL	
Professional Body Membership:	NIL			

Staff Achievements:	Got pass percentage of 100% in Digital signal processing. Got pass percentage of 100% in signals and systems			
Workshop Details:	Sl.No.	Title	Venue	Month/Year
	1	Industrial Automation	Tagore Engineering College	12 th March/2011
	2.	Analog and Digital IC Design using CADENCE tools	Chettinad College of Engineering and Technology	7-8 october/2013
	3.	Training cum orientation workshop in faculty development	Indira Gandhi College of Engineering and Technology for Women	19 th – 26 th June/2015

FDP Details:	Sl.No.	Title	Venue	Month/Year
	1.	FDP on “Internet of Things”	Sri Sairam Engineering	23 rd -24 th November/2016
	2.	National Level FDP on “Teaching Excellence in Engineering and Management curriculum”	Sri Sairam Engineering	20 th -21 st June/2016
	3.	FDP on Web Application security	Jeppiaar Institute of Technology	27 th -28 th May 2020
	4.	FDP on Data Science using Python	Jeppiaar Institute of Technology	18 th -22 nd May 2020
	5.	FDP on “Recent trends in information, computing, communication and Teaching methodology competency	Sri sairam Engineering College	4 th to 17 th May 2020
	6.	FDP on “Next Generation Intelligence”	St. Peter’s Institute Of Higher Education And Research	20 th May to 25 th May 2020

Conference Details:	Sl. No.	Title	Venue	Month/Year
	1.	Memory based realisation of FIR digital filter using OMS scheme	SMK Fomra Institute of Technology	3 rd April/2012
	2.	Synthesis and simulation of hyperbolic tangent activation function using hybrid approximation in VLSI	Syed Ammal Engineering College	30 th Jan-1 st Feb/2014
	3.	Implementation of PWL and LUT based Approximation for Hyperbolic Tangent Activation Function in VLSI	Adhiparasakthi Engineering College	3 rd -5 th April/2014
	4.	A Novel Algorithm for Computing 8 Point DCT	A.C.T College of Engineering and Technology	9 th -10 th April/2015
	5.	Efficient VLSI implementation of fast radix-2 DCT	Indira Gandhi College of Engineering and Technology for Women	20 th March/2015

	6.	Auto Theft Prevention using Gravitational Lock	Indira Gandhi College of Engineering and Technology for Women	20 th March/2015
	7.	Traffic density analysis using background subtraction algorithm	St. Joseph college of Engineering	22 nd March/2019
	8.	Cost effective smart home based on FPGA	St. Joseph college of Engineering	22 nd March/2019

	9.	Driverless vehicle for Garbage Collection	Sri Sairam Engineering College	20 th to 22 nd February 2020
		Raspberry Pi Based Smart Walking Stick for Visually Impaired Person	Musaliar College of Engineering and Technology, Pathanamthitta, Kerala.	24th & 25th Sep 2021
		Personality Prediction Through CV Analysis using Machine Learning Algorithms for Automated E-Recruitment Process	Sri Sai Ram Engg. College	16th & 17th Dec 2021
		Arduino Based Circuit Breaker for Power Distribution Lines to Enhance the Safety of Lineman	Francis Xavier Engineering College, Tirunelveli	10th & 11th Feb 2022
		A Novel Algorithm with Complexity Reduction for Multimedia Applications	Sri Sai Ram Engg. College	10th & 11th March 2022

		Identification of Currency Note for Blind People	Symbiosis Institute of Technology, Pune	21st & 22nd June 2022
		Smart Guiding Assistant for Visually Impaired using Raspberry Pi	Sri Sai Ram Engg. College	09th Nov to 10th Nov 2022
		Smart Blind Cane for Blind and Deaf People with a Safety Trigger	Sri Sai Ram Engg. College	08th Dec to 09th Dec 2022
		Cloud Assisted IoT Enabled Forest Fire Detection System	Sri Sai Ram Engg. College	08th Dec to 09th Dec 2022

Journal Details:	Sl.No.	Title	Journal Name	Volume/ Date
	1.	Synthesis and simulation of hyperbolic tangent activation function using hybrid approximation in VLSI	International Journal of Electrical and Communication Engineering for Advanced Research	Vol. 2, SP-1, Feb. 2014
	2.	Traffic density analysis using background subtraction algorithm	International Journal of Scientific Research and Review	Vol. 8, Issue 3, March 2019
	3.	Cost effective smart home based on FPGA	International Journal of Scientific Research and Review	Vol. 8, Issue 3, March 2019
	4.	Traffic Density Analysis Using Canny Edge Detection Algorithm	Test Engineering and Management	Volume 82 Page Number: 16859 - 16863 Publication Issue:

		Zigbee based Automation Systems for Homes with the Deployment of Smart Sensors	Materials Today: Proceedings (SCOPUS INDEXED)	January - February 2020 https://doi.org/10.1016/j.matpr.2021.01.043
		Driverless Vehicle for Garbage Collection	Materials Today: Proceedings (SCOPUS INDEXED)	https://doi.org/10.1016/j.matpr.2021.01.044
		Personality Prediction Through CV Analysis using Machine Learning Algorithms for Automated E-Recruitment Process	IEEE Xplore Digital Library (SCOPUS INDEXED)	1.044Electronic ISBN: 978-1-6654-1447-0,
		A Novel Algorithm with Complexity Reduction for Multimedia Applications	IEEE Xplore Digital Library	DOI: 10.1109/IC

		Smart Footwear for Patients with Diabetic Neuropathy	Indian Journal of Natural Sciences	Electronic ISBN: 978-1-6654-7995-
		A Smart Navigation Gadget for Visually Impaired People	Indian Journal of Natural Sciences	Electronic ISBN: 978-1-6654-7995-
		Arduino Based Circuit Breaker for Power Distribution Lines to Enhance the Safety of Lineman	Lecture Notes on Data Engg. and Communications Technologies	Volume 131, https://doi.org/10.1007/978-981-19-1844-5_55
		Raspberry Pi based Smart Walking Stick for Visually Impaired Person	AIP Conference Proceedings	20 August 2022, https://doi.org/10.1063/5.0103097

Patent Details:	Sl.No.	Title	Country	Applied Date
	1	An Internet of Things and Machine Learning Based Self-Driven Warehouse Robot Vehicle	India	15.05.2022

Additional Details:

- Delivered a Workshop on the topic “**Verilog HDL design based entry and simulation** “ in an International conference on communication, computing and internet of things