

A Six day FDTP on CMOS Analog IC Design

Tambaram Talk –Nov 2019

Week of learning



| TALK TEAM |

Around 30 faculties from various institutions in and-around Chennai, took part in the six-day Faculty Development Training Pro-

gramme (FDTP), organised by department of Electronics and Communication Engineering of Sri Sairam Engineering College, on 'CMOS Analog IC Design', jointly sponsored by Anna University, Chennai, from

25 to 30 November.

A press release said, the FDTP was inaugurated by chief guest, Chip Test CEO M Sankar Subramanian.

'The objective of this FDTP, is to enrich the knowledge of faculty members of various engineering colleges affiliated to the University, through interaction with eminent resource persons from industry and academics in the field of IC Design,' the release added.

The FDTP concluded with the valedictory function on 30 November headed by the principal A Rajendra Prasad. Head of the department J Raja, coordinators S Usha, and N Logeswari ECE were also present.